

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): MAKIHARA, et al.  
Application No.: 10/594,116  
Filed: September 25, 2006  
For: METHOD FOR MANUFACTURING A SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE

**RESPONSE TO NOTIFICATION OF MISSING REQUIREMENTS**

Mail Stop Missing Parts  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 7, 2007

Sir:

In response to the Notice to File Missing Parts dated June 8, 2007, in connection with the above-identified application, attached hereto is an executed Declaration for the above-identified patent application, in compliance with 37 CFR 1.63.

The fee of \$130.00 required to satisfy the requirements set forth in 37 CFR 1.16(f) is being submitted electronically at the time of filing this response.

Please charge any shortages in the fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (Docket No. 045.46647X00) and please credit any excess fees to such deposit account.

Respectfully submitted,

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